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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Complete if Known	
		Application Number	10/668,817
		Filing Date	September 22, 2003
		First Named Inventor	David Zimmerman
		Art Unit	Unknown 28 63
		Examiner Name	To Be Determined 764u
		Attorney Docket Number	42P16614
Sheet	2	of	2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
TL		U.S. Patent Serial No. 10/224,492 - Tripp, et al., Filing Date 8/21/2002 entitled "Device Testing"	
TL		U.S. Patent Serial No. 10/319,517 - Nejedlo, et al., Filing Date 12/16/2002 entitled "Testing Methodology and Apparatus for Interconnects"	
TL		Doran, Mark - <i>Extensible Firmware Interface EFI 1.10 and Beyond: An Overview</i> , Intel Developer Forum, September 9-12, 2002, pgs. 1-26	
TL		Hong, Yeoh Eng, et al. - <i>An Overview of Advanced Failure Analysis Techniques for Pentium® and Pentium® Pro Microprocessors</i> , Intel Technology Journal Q2, '98, pgs. 1-11	
TL		Sun Microelectronics MicroSPARC-Ilep™ - <i>Introduction to JTAG Boundary Scan</i> , Sun Microsystems, Inc. White Paper, January 1997, pgs. 1-8	

Examiner Signature		Date Considered	1-13-05
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.
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